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Graves Award
Computer Science Major
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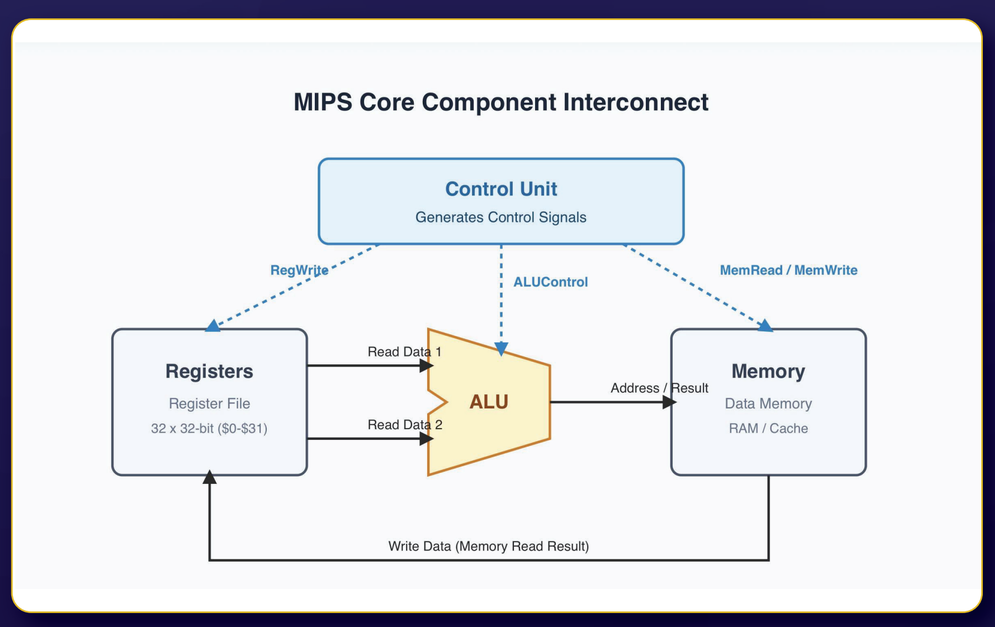
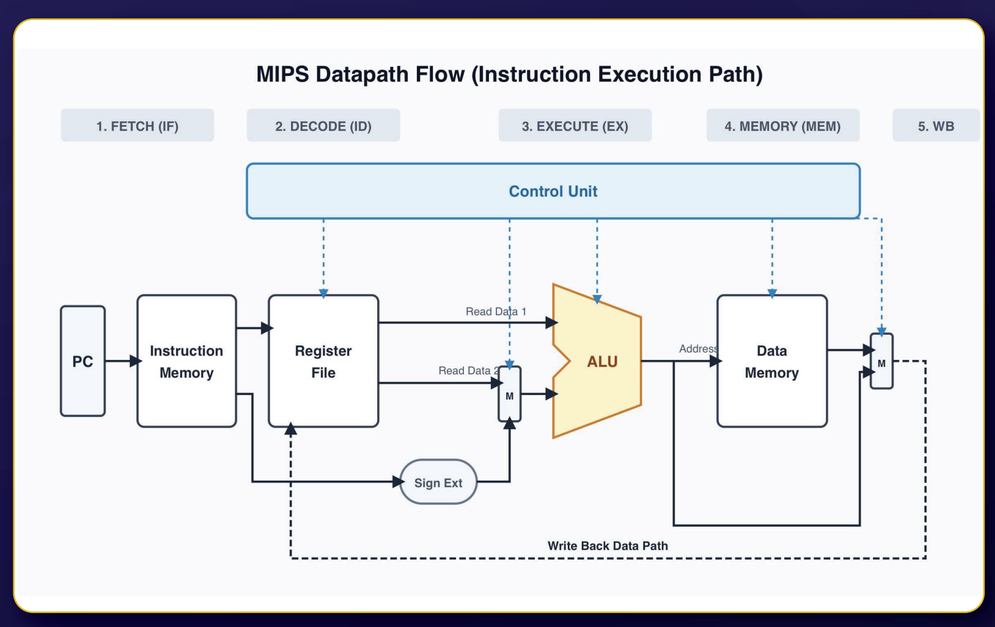
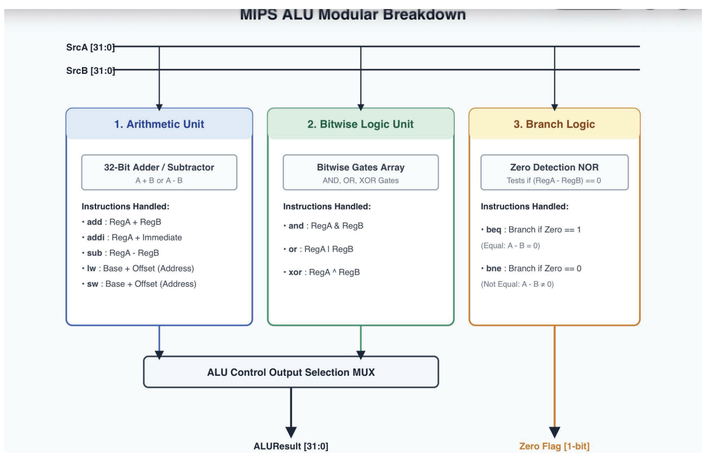
MOREHOUSE COLLEGE

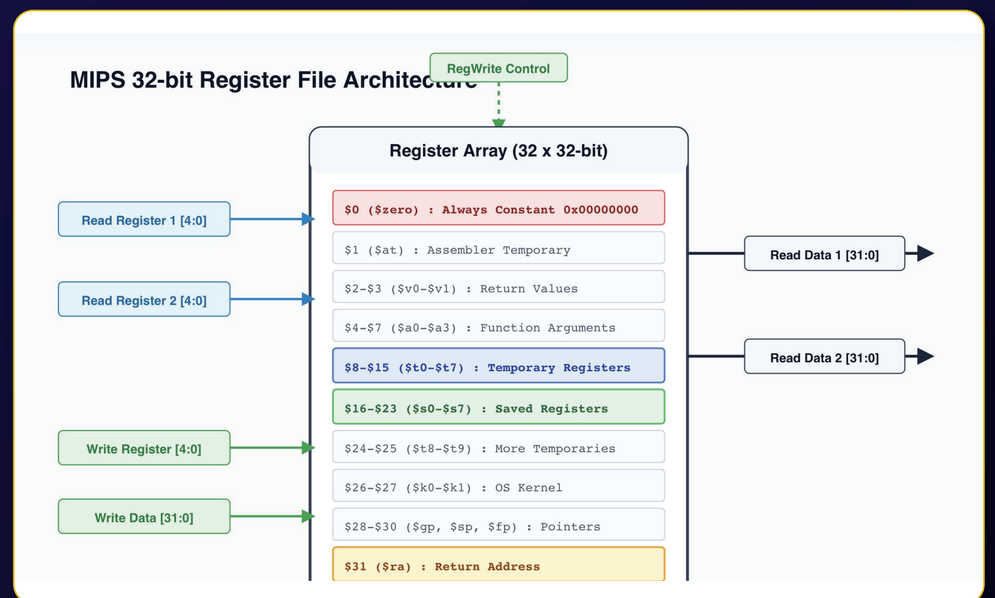
MIPS Processor Model

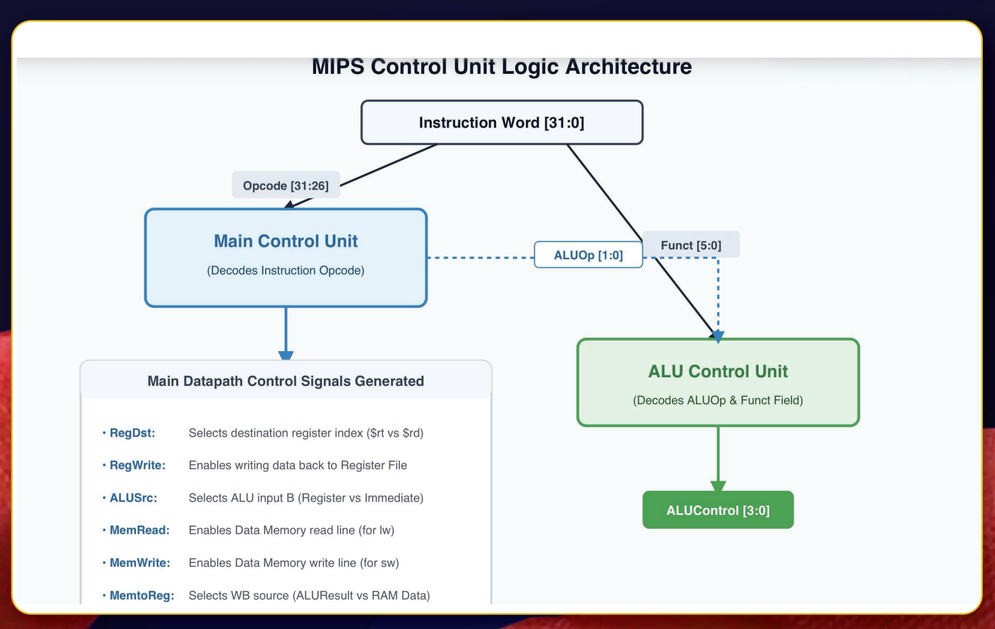
A custom 32-bit MIPS processor model, developed as an understanding of computer hardware and to demonstrate core principles of computer architecture. This project showcases practical hardware simulation, featuring a fully functional datapath for instruction decoding, memory management and execution.

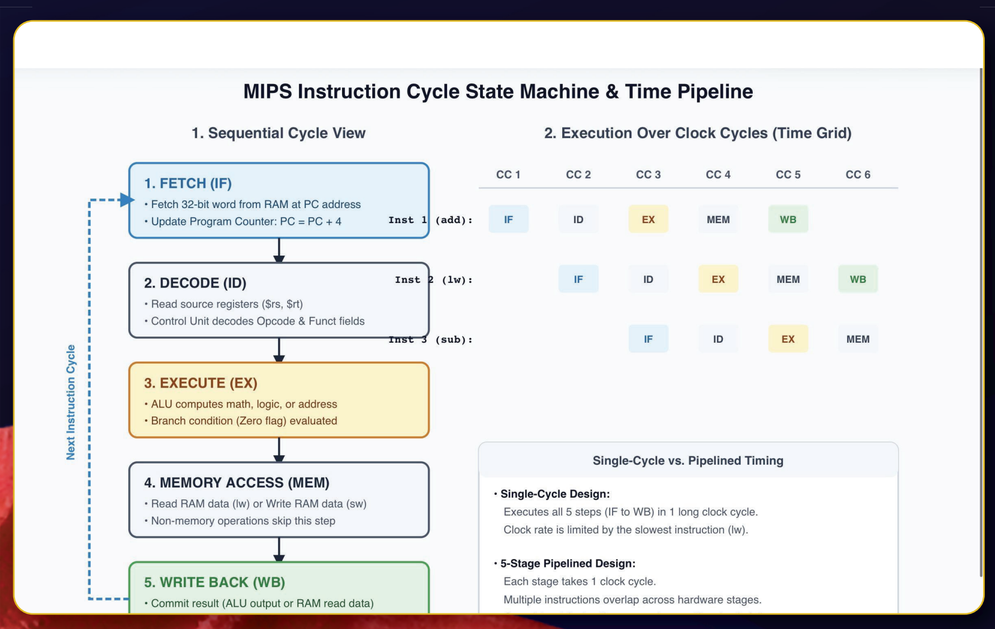












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